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Micro electromechanical system switch.

The invention relates to a micro electromechanical system (MEMS) switch.

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Micro electro mechanical systems (MEMS) are a known class of device typically made by silicon processing techniques in semiconductor materials, and developed as alternatives to conventional electro mechanical devices such as shutters, actuators, relays, valves etc. and thermo mechanical devices such as bimetallic beams.

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Examples of such MEMS are used in switches where an electrical contact needs to be made to e.g. a signal line in a microwave system. Two parts of the microwave system need to be connected and disconnected as required by mechanically moving a switch contact on a moveable arm or armature onto and away from fixed stationary contact pad. Electrostatic actuation is a known technique used in MEMS switches. Such actuation provides a force between two conducting objects proportional to the rate of change of capacitance and the square of the applied voltage difference. The force between two charged conductors varies with the inverse square of their separation.

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Sticking contacts are frequently the cause of failure in MEMS switch devices. Such stiction may arise from surface interactions at the contact interface such as Van der Waals forces or can be brought about by high current densities or elevated temperatures at the contact ('hot' contact welding). Switches are typically designed to provide sufficient mechanical stiffness to provide a suitable static restoring force.

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Careful selection of contact material, and ensuring that the contact has maximum area (for low current densities) goes some way toward minimising the likely stiction force that must be overcome to release the switch, but does not totally eliminate it.

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The structure could be designed to have a high static mechanical return force under closed conditions, but this tends to lead to a structure which require very high actuation voltages (assuming electrostatic actuation).

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Maximum force is obtained from an electrostatic actuator when: the armature contact is in close proximity with the stationary contact, and the armature contact is parallel to the stationary contact.

- 5 The need for good isolation characteristics when the switch is in the open position requires that the gap between switch contacts be around $3\mu\text{m}$ or more. The requirement for minimum switching time places an upper limit on the size of the armature and the requirement for good return force imposes a lower limit on its stiffness.

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Transverse piezoelectric actuation of an armature is another known technique for operating MEMS switches. Such actuation leads to a parabolic curvature of the armature in the region under actuation. The curvature is such that the beam tip deflection varies in direct proportion to the voltage across the piezoelectric layer.

- 15 Contact forces resulting from piezoelectric actuation tend to be low. It is desirable that contact forces be high to ensure good electrical contact.

The above problems are reduced, according to this invention, by the use of a dual actuation mechanism; namely electrostatic operation in conjunction with piezoelectric, 20 or electrothermal or electromagnetic operation, together with a curvature of the armature.

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According to this invention a MEMS switch includes a fixed contact and a moveable contact on an armature characterised by: -

5 electrodes associated with both the fixed and moveable contacts for providing an electrostatic switch operation;

piezoelectric material with associated electrodes for bending the armature upon application of an electric voltage and providing a piezoelectric switch operation;

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the armature being of curved shape which is bent away from the fixed contact when in a switch open condition with zero applied voltage;

15 the arrangement being such that operation of the piezoelectric material bends the armature towards the fixed contact and bends the moveable electrode into both close proximity and substantially parallel alignment with the fixed electrode for clamping of the fixed and movable contacts under electrostatic force.

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The invention will now be described, by way of example only, with reference to the accompanying drawings of which: -

Figure 1 shows a schematic view of a MEMS switch in its open or zero applied voltage state, and its armature curved away from its fixed contact;

5 Figure 2 shows the switch of Figure 1 in a partly closed state with electrostatic electrodes in a parallel spaced apart position;

10 Figure 3 shows the switch of Figure 1 in its fully closed condition;

Figure 4 shows a cross sectional view of a MEMS switch as fabricated before release as in the partly switched state of Figure 2; and

15 Figure 5 shows a plan view of the switch of Figure 4.

As shown in Figures 1-3 a dual electrostatic and piezoelectric actuated MEMS switch 1 comprises a substrate 2 carrying a fixed contact 3 and a lower electrostatic electrode 4 itself coated with a thin insulating layer 5. An armature 6 has one end 20 fixed to the substrate and a free outer end carrying a moveable dimple contact 7. The armature 6 itself is of a sandwich construction, an upper metal layer 8, a piezoelectric layer 9 of PZT, a central metal layer 10, an insulating mechanical layer 11, and a lower metal layer 12 which carries the moveable contact 7.

25 The switch 1 in its open, voltage off, state is manufactured to have a curved profile as shown in Figure 1. This curvature is obtained by control of manufacturing steps to give different amounts of stress in the component layers 8-12.

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Figure 2 shows a partly switched state in which voltages have been applied to the electrodes 8, 10 across the piezoelectric layer 9 causing expansion of the layer's length and a consequential straightening of the armature 6. In this condition the armature 6 is straight and both the lower electrostatic electrode 4 and the lower layer 12 (forming a part of the electrostatic actuator) are parallel to one another with a small separation. At this stage the electrostatic electrodes 4, 12 are energised to draw the armature 6 down into the switch closed condition shown in Figure 3. After closure, Figure 3, the piezoelectric element 9 is switched off by removing its applied voltage, and the switch remains in a clamped, closed, condition by continued application of a voltage to the electrostatic electrodes 4, 12.

In this closed, Figure 3, condition component parts of the armature are in a strained state. Thus on removal of the electrostatic switch voltage, the contacts separate as the armature springs back to its free, Figure 1, condition. If required, a reverse voltage may be applied to the piezoelectric material 9 to assist in bending the armature 6 away from the fixed contact 3.

Typically, MEMS switches operate with measured insertion losses of below 0.2 dB over broadband ranges up to 45 GHz with isolation of >50 dB under 2 GHz.

A sectional schematic of an example MEMS switch according to the invention 20 is shown in Figure 4 as fabricated, before release. It is comprises a silicon substrate 21 capped with an electrical isolating layer 20 of silicon dioxide carrying a patterned metal layer including a coplanar waveguide radio frequency transmission line (or CPW) and outer and inner switch electrodes 25, 26 respectively. The CPW includes two ground planes 22, 23 and a transmission line 24. Ground plane 22 is locally covered with a thin e.g. 0.2 μ m layer 27 of insulating silicon nitride and forms a lower electrode for electrostatic actuation.

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An armature 30 is spaced apart above the substrate 21 with one end fixed on the switch electrodes 25, 26 and the other end free to move up or down under electrical control. The armature 30 is formed by a 1µm thick layer of silicon nitride carrying on its lower surface a patterned first metal layer 33. This layer 33 is formed into an 5 electrode 34 providing an upper electrostatic electrode and is electrically connected to the inner switch electrode 26. The first metal layer also forms a moveable electrode 35 with dimples arranged to contact either side of a break 42 in the transmission line 24 when the switch is operated. On top of the silicon nitride layer 38 is a second metal layer which connects through a via 35 to the upper electrostatic electrode 34 10 and is patterned to form a lower electrode 36 under a 1µm thick layer 37 of lead-zirconate-titanate (or PZT) piezoelectric material. The PZT layer 37 is capped with a dielectric layer 39. Above the piezoelectric layer 37 is an upper piezoelectric actuation electrode 40 which connects to the outer switch electrode 25 through vias 31, 41. The capping layer 39 isolates the lower piezoelectric actuation electrode 36 15 from the upper piezoelectric actuation electrode 40. Reference number 43 indicates a sacrificial layer, used during processing steps, then removed as described later.

In operation the armature 30 is bent upwards, as in Figure 1, when in a voltage off condition. To close the switch, voltages are applied to the two anchor electrodes 25, 20 26 causing the PZT layer 37 to expand and bend the armature 30 into the Figure 4 state or even lower. In this condition the electrostatic electrodes 22 and 34 are in substantially parallel close proximity. An applied voltage difference between the CPW ground plane 22 and the upper electrostatic electrode 34 causes attraction between electrodes which supplements the downwards movement of the armature until the 25 moveable switch contacts 35 are in contact with the transmission line 24 and the switch is in its closed state. At this point the voltage can be removed from the PZT layer 37 (by removing the signal to the outer anchor pad 25) and the switch remains firmly clamped by electrostatic forces.

30 To open the switch, the voltage is removed from the electrostatic electrode 26, 34 allowing strain energy stored in the armature 30 to move the moveable contact 35 upwards and out of engagement with the transmission line 24. If necessary, the PZT layer 37 may be reverse biased to contract in length and reinforce the upward movement of the armature 30.

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The switch of Figure 4 may be produced by the following production steps:

All layers are fabricated on commercially available virgin silicon wafers. These are typically boron doped (p-type) with a resistivity of $<0.1\Omega\text{cm}$ to allow an optional ohmic

5 metal contact to be made to the substrate 21.

An electrical isolation layer of silicon dioxide film is grown/deposited on the wafers. Contact holes may be etched (e.g. by reactive ion etching, RIE) in this layer to enable a bulk substrate contact to be made in subsequent process steps.

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A metal film (components 22-26) is deposited next (e.g. by sputter deposition), and is then patterned using photolithography. In this process, the wafers are coated with photoresist, the photoresist is exposed with the appropriate mask, and the exposed photoresist is developed to create the desired etch mask for subsequent

15 pattern transfer into the underlying layer. After patterning the photoresist, the underlying layer is etched (e.g. by RIE) and the photoresist removed (e.g. by RIE). This sequence of lithography, deposition and etch is repeated to build up a three dimensional (3D) structure on the surface of the wafer. This fixed metal layer forms electrodes interconnects and bond pads.

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A thin dielectric layer 27 (such as Silicon Nitride) is deposited (e.g. by Plasma Enhanced Vapour Deposition or PECVD), and patterned using photolithography. This layer protects the fixed metal from unintended electrical contacts and insulates the lower electrostatic electrode 22.

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A sacrificial layer 43 (such as polyamide, amorphous silicon etc) is then deposited (e.g. by resist spinning). This layer may provide a degree of planarisation, and is removed in a release process (such as a RIE release or wet etch release process) at the end of the fabrication process to free the structural moving layers

30 forming the armature 30.

Dimples may be formed in the sacrificial layer by photolithography and a timed etch of the sacrificial layer.

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Contact holes (31, 32) are etched in the sacrificial layer, to enable electrical and mechanical connections between the moving mechanical layers and the fixed metal layer.

5 A conducting metal layer (33) is deposited (e.g. by sputtering) and patterned by photolithography next. This layer forms both the upper, movable, electrode 34 for electrostatic actuation, and the conducting layer 35 for RF switching operations.

10 A mechanical dielectric layer (such as PECVD silicon nitride) is deposited and patterned next. This layer forms the elastic mechanical layer 38 in the switch armature. In plane stresses and out of plane stress gradients may be controlled in this layer as described in [1,2] to enable some control of curvature in the released switch armature. Varying process parameters in the PECVD deposition process (e.g. RF power) allows such control.

15 A conducting layer is deposited and patterned. This layer forms the bottom electrode 36 for the piezo-electric material layer 37 in the switch. This layer may comprise a combination of conducting layers including non-metals (such as Lanthanum Nickelate) and must provide a suitable surface for nucleation of the 20 appropriate phase in the piezo-electric material layer 37 (e.g. perovskite).

A piezo-electric layer 37 is deposited (e.g. by Metal Organic Chemical Vapour Deposition) and patterned (e.g. by photolithography and RIE).

25 A thin capping dielectric layer 39 (e.g. PECVD silicon nitride) is deposited and patterned, to prevent unintended electrical contact between the lower piezoelectric electrode 36 and subsequent conducting layers. The stress in this layer may be controlled to enable further control of curvature in the released switch armature.

30 A third metal layer is deposited and patterned. This layer forms the top piezoelectric electrode 40 and in conjunction with electrode 36 allows an electrical field to be applied across the piezo-electric layer 37. This layer makes contact with the outer anchor pad 25.

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A subsequent dielectric layer may be deposited (with controlled stress) and patterned to further control the regions and extent of curvature in the released switch armature.

- 5 Following the above process allows for a switch armature to be created with a small sacrificial layer thickness, but which tends to curve upwards over the region of the armature including a piezo-electric layer (but is substantially flat elsewhere). This upward curvature leads to a large gap (>3 microns) between the movable contact 35 switching the RF and the fixed metal layer 24 carrying the RF. This enables good
- 10 isolation and/or insertion loss to isolation ratio.

References

- [1] R.J. Bozeat, K.M. Brunson; "Stress control in low temperature PECVD silicon nitride for highly manufacturable micromechanical devices", Micromechanics Europe, Ulvic (Norway), 1998.
- [2] R.R. Davies, K.M. Brunson, M. McNie, D. J. Combes; "Engineering In- and Out-of-Plane stress in PECVD Silicon Nitride for CMOS-Compatible Surface Micromachining", SPIE Microfabrication and Micromachining Oct 2001, California, USA.

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